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APPLICATION NO.	F	ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
10/614,384	,	07/07/2003	Steven R. Hilliard	27234.01	7445	
22465	7590	01/23/2004		EXAMINER		
PITTS AND P O BOX 51		TAN P C		DEB, AN	IJAN K	
KNOXVILL		7950-1295		ART UNIT PAPER NUMBER		
				2858		

DATE MAILED: 01/23/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

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~	Application No.	Applicant(s)	00, 0	
	10/614,384	HILLIARD ET AL.	HILLIARD ET AL.	
Office Action Summary	Examiner	Art Unit		
	Anjan K Deb	2858		
The MAILING DATE of this communication Period for Reply	n appears on the cover sheet t	with the correspondence ac	idress	
A SHORTENED STATUTORY PERIOD FOR R THE MAILING DATE OF THIS COMMUNICATI - Extensions of time may be available under the provisions of 37 C after SIX (6) MONTHS from the mailing date of this communicatio - If the period for reply specified above, its less than thirty (30) days, - If NO period for reply is specified above, the maximum statutory p - Failure to reply within the set or extended period for reply will, by - Any reply received by the Office later than three months after the earned patent term adjustment. See 37 CFR 1.704(b). Status	ON. FR 1.136(a). In no event, however, may on. a reply within the statutory minimum of the period will apply and will expire SIX (6) Mc statute, cause the application to become	a reply be timely filed nirty (30) days will be considered time DNTHS from the mailing date of this c ABANDONED (35 U.S.C. § 133).		
1) Responsive to communication(s) filed on	<u>07 July 2003</u> .			
2a)☐ This action is FINAL . 2b)⊠	This action is non-final.			
3) Since this application is in condition for al closed in accordance with the practice un			e merits is	
Disposition of Claims				
4) ⊠ Claim(s) <u>1-38</u> is/are pending in the applic 4a) Of the above claim(s) is/are wit 5) □ Claim(s) is/are allowed. 6) ⊠ Claim(s) <u>1-38</u> is/are rejected. 7) □ Claim(s) is/are objected to. 8) □ Claim(s) are subject to restriction a	thdrawn from consideration.			
Application Papers				
9) The specification is objected to by the Exact 10) The drawing(s) filed on is/are: a) Applicant may not request that any objection to Replacement drawing sheet(s) including the county The oath or declaration is objected to by the	accepted or b) objected to the drawing(s) be held in abey correction is required if the drawing.	ance. See 37 CFR 1.85(a). ng(s) is objected to. See 37 C		
Priority under 35 U.S.C. §§ 119 and 120				
12) Acknowledgment is made of a claim for for a) All b) Some * c) None of: 1. Certified copies of the priority docu 2. Certified copies of the priority docu 3. Copies of the certified copies of the application from the International B * See the attached detailed Office action for 13) Acknowledgment is made of a claim for do since a specific reference was included in the 37 CFR 1.78. a) The translation of the foreign language 14) Acknowledgment is made of a claim for do reference was included in the first sentence	ments have been received. ments have been received in e priority documents have been pureau (PCT Rule 17.2(a)). a list of the certified copies no mestic priority under 35 U.S.C he first sentence of the specif ge provisional application has mestic priority under 35 U.S.C	Application No en received in this National ot received. c. § 119(e) (to a provisional ication or in an Application been received. c. §§ 120 and/or 121 since	al application) Data Sheet.	

U.S. Patent and Trademark Office PTOL-326 (Rev. 11-03)

1) Notice of References Cited (PTO-892)
2) Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) Information Disclosure Statement(s) (PTO-1449) Paper No(s) 09/26/2003.

Attachment(s)

6) Other:

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DETAILED ACTION

Claim Rejections - 35 USC § 112

1. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

2. Claims 35,38 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claims 35,38 recites the limitation "resistance-inductance-capacitance" in driver circuit. There is insufficient antecedent basis for this limitation in the claim.

Double Patenting

3. The nonstatutory double patenting rejection is based on a judicially created doctrine grounded in public policy (a policy reflected in the statute) so as to prevent the unjustified or improper timewise extension of the "right to exclude" granted by a patent and to prevent possible harassment by multiple assignees. See *In re Goodman*, 11 F.3d 1046, 29 USPQ2d 2010 (Fed. Cir. 1993); *In re Longi*, 759 F.2d 887, 225 USPQ 645 (Fed. Cir. 1985); *In re Van Ornum*, 686 F.2d 937, 214 USPQ 761 (CCPA 1982); *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970);and, *In re Thorington*, 418 F.2d 528, 163 USPQ 644 (CCPA 1969).

A timely filed terminal disclaimer in compliance with 37 CFR 1.321(c) may be used to overcome an actual or provisional rejection based on a nonstatutory double patenting ground provided the conflicting application or patent is shown to be commonly owned with this application. See 37 CFR 1.130(b).

Effective January 1, 1994, a registered attorney or agent of record may sign a terminal disclaimer. A terminal disclaimer signed by the assignee must fully comply with 37 CFR 3.73(b).

Claims 1-38 are rejected under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claims 1-35 of U.S. Patent No. 6,590,400

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B2. Although the conflicting claims are not identical, they are not patentably distinct from each other because they are broader in scope and encompass all of the limitations claimed in U.S. Patent No. 6,590,400 B2.

Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 5. Claim 36 is rejected under 35 U.S.C. 102(b) as being anticipated by Toppeta (US 4,263,549).

Re claim 36, Toppeto et al. discloses (Fig. 1) inductance measurement circuit for measuring an inductance of a wire-loop 12, comprising pair of driver circuits 24,26 in electrical communication with wire-loop 12, demodulation circuit 10 in electrical communication with said pair of driver circuits producing demodulated signal, and filter (column 2 lines 14-17) in electrical communication with the demodulation circuit.

Claim Rejections - 35 USC § 103

- 6. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

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7. Claims 1, 37-38 are rejected under 35 U.S.C. 103(a) as being unpatentable over Toppeto et al. US 4,263,549 in view of Kogure (US 5,469,156).

Re claims 1, 37-38, Toppeto et al. discloses all of the claimed limitations including as set forth above including wire-loop 12, pair of resistance-inductance-capacitance (46,48,34,36,38) driver circuits 24,26 in electrical communication with wire-loop 12, and demodulation circuit 10 in electrical communication with said pair of driver circuits producing demodulated signal.

Toppeto et al. did not expressly disclose A/D converter.

Kogure discloses measuring inductance (field sensor 108) comprising A/D converter 105.

At the time of the invention it would have been obvious for one of ordinary skill in the art to modify Toppeto et al. by adding A/D converter disclosed by Kogure for processing measured signals by microprocessor (column 5 lines 15-20) for calculating loop inductance.

Pertinent Art

8. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Clark (US 4,568,937) discloses circuit for measuring inductance of wire-loop 1, comprising plurality of driver circuits 2 (Fig. 3-5) in electrical communication with wire-loop 12, and R,L,C demodulation circuit (Fig. 5) in electrical communication with driver circuits.

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Beck (US 5,621,314) discloses circuit for measuring loop inductance (Fig. 1-2)

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comprising evaluation device 6 (Fig. 2) which is essentially an impedance measuring

device, having measurement voltage source 12 followed by a loop end stage 13 and a

detector 14 which includes a signal amplifier 15 for measuring the loop voltage.

Contact Information

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Dr. Anjan K. Deb whose telephone number is (703) 305-

5219. If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, N. Le, can be reached at (703)-308-0750.

Any inquiry of a general nature or relating to the status of this application should

be directed to the Group receptionist whose telephone numbers are (703)-308-0956 and

(703)-305-4900.

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Art Unit: 2858

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1/21/04

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